

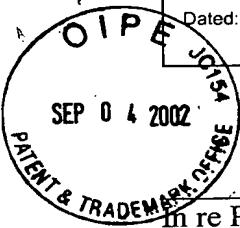
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Dated: August 30, 2002

Signature

*Arnold Dompieri*  
(Arnold B. Dompieri)

2183  
#S  
BT  
Docket No.: SCEI 3.0-058  
(PATENT) 9.13.02



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Suzuki et al.

Application No.: 09/816,752

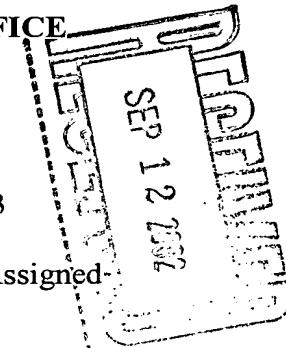
Filed: March 22, 2001

For: PROCESSING MODULES FOR COMPUTER  
ARCHITECTURE FOR BROADBAND  
NETWORKS

Commissioner for Patents  
Washington, DC 20231

Group Art Unit: 2183

Examiner: Not Yet Assigned



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SEP 06 2002

Technology Center 2100

### INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. The cited references were listed on the PCT International Search Report issued in connection with applicant's corresponding PCT application. A copy of each reference and a copy of the PCT search report are enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: August 30, 2002

Respectfully submitted,

By

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Substitute for form 1449A/PTO				<b>Complete if Known</b>	
				Application Number	09/816,752
				Filing Date	March 22, 2001
				First Named Inventor	Masakazu Suzuki
				Art Unit	2183
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	SCEI 3.0-058

<b>U. S. PATENT DOCUMENTS</b>					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			

<b>FOREIGN PATENT DOCUMENTS</b>					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)			
BA	JP 56-123051	09-26-1981	Omron Tateisi Electronics Co.		T <sup>6</sup>
BB	JP 05-242057	09-21-1993	Sanyo Electric Co., Ltd.		**
BC	EP 0077404	04-27-1983	Fanuc Ltd.		**
BD	JP 61-180352	08-13-1986	Fujitsu Limited	RECEIVED	**
BE	JP 02-210542	08-21-1990	Fujitsu Limited		**
BF	JP 05-151183	06-18-1993	Hitachi, Ltd.		**
BG	JP 63-019058	01-26-1988	Fujitsu Limited		**
BH	JP 54-146555	11-15-1979	NEC Corporation	SEP 06 2002	Technology Center 2100
BI	JP 10-334055	12-18-1998	Sony Corporation		**
BJ	EP 0871142A2	10-14-1998	Sony Computer Entertainment Inc.		**
BK	JP 08-161283	06-21-1996	Sony Corporation		**
BL	JP 08-235143	09-13-1996	NEC Kofu, Ltd.		**
BM	JP 07-287064	10-31-1995	Mitsubishi Electric Corporation		**
BN	JP 02-012361	01-17-1990	Fujitsu Limited		**

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See attached Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the application number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

<b>OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>2</sup>
	CA	MASAKAZU SUZUOKI, Development of a Microprocessor with a 128b CPU, 2 Vector Processors with 10 Floating Point Units, IPSJ Journal, Information Processing Society of Japan, 04-15-2000, pages 843-850, ISSN 0387-5806, Vol. 41, No. 4.			X

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

\*\*English Translation of Abstract Only